NOTES: UNLESS OTHERWISE SPECIFIED

1. SPECIFICATIONS/TOLERANCES:
   A. FABRICATE PER IPC-6012, CLASS 2, USING PROVIDED DATA FILES 600-XXXXX-01_01.TGZ OR 610-XXXXX-01_01.ZIP.
   B. ALL SPECIFICATIONS USED SHALL BE PER THEIR LATEST REVISIONS.
   C. THE DIMENSIONS OF CIRCUIT FEATURES IN THE PROVIDED DATA MAY BE ADJUSTED ONLY TO COMPENSATE FOR PROCESS TOLERANCES; ADDING, REMOVING OR RELOCATING CIRCUIT FEATURES, INCLUDING NON-FUNCTIONAL PADS, IS NOT ALLOWED, AND THE DESIGN OF ALL PLANE STRUCTURES MUST BE MAINTAINED TO ENSURE PROPER ELECTRICAL PERFORMANCE.
   D. REMOVE ALL BURRS AND BREAK SHARP EDGES, .381 [.015] MAX RADIUS.
   E. PARENTHETICAL INFORMATION IS FOR REFERENCE ONLY.
   F. REPAIR OF PCB DEFECTS IS NOT PERMITTED.

2. DIELECTRIC MATERIAL:
   A. DIELECTRIC MATERIAL SHALL BE PER IPC-4101/99, /124, /126 OR /129 (RoHS COMPLIANT EPOXY-GLASS).
   B. MINIMUM DIELECTRIC THICKNESS SHALL BE .051 [.002] FOR REFERENCED STACK-UP DIMENSIONS OF.076 [.003] OR GREATER; IPC-6012 REQUIREMENTS SHALL OTHERWISE APPLY. SINGLE-PLY CONSTRUCTION IS ALLOWED.
   C. SEE LAYER STACK-UP FOR REQUIRED COPPER WEIGHTS AND THE FINISHED PCB THICKNESS. IF SPECIFIED. 1/3 OZ. STARTING FOIL MAY BE ACHIEVED BY 1/2 OZ. FOIL REDUCTION.
   D. FINISHED PCB THICKNESS SHALL BE MEASURED OVER LANDS AND/OR CONDUCTORS COVERED BY SOLDER MASK.
   E."MATERIAL TO BE USED IS ITEO IT 180A"

3. DRILLING:
   A. VIA DIAMETERS (TOL. + +.051/- DRILL DIAMETER [+.0020/- DRILL DIAMETER]) SHALL BE VERIFIED BEFORE PLATING; ALL OTHER HOLE DIAMETERS SHALL BE VERIFIED AT FINAL INSPECTION.
   B. LAYER-TO-LAYER MISREGISTRATION SHALL BE .127 [.005] MAXIMUM.

4. SOLDER MASK:
   A. APPLY LPI SOLDER MASK USING PROVIDED DATA.
   B. SOLDER MASK SHALL BE PER IPC-SM-840, CLASS T, COLOR BLUE.
   C. THE DIMENSIONS OF SOLDER MASK-DEFINED PADS ON PLANES AND/OR WIDE CONDUCTORS SHALL NOT BE MODIFIED.

5. MARKING:
   A. MARK PCB PER PROVIDED DATA USING SILKSCREEN OR AUTOMATED INJET PROCESSING WITH PERMANENT, NON-CONDUCTIVE INK, COLOR WHITE.
   B. SUPPLIER ID AND TRACEABILITY INFORMATION SHALL BE APPLIED USING PERMANENT, NON-CONDUCTIVE INK, COLOR WHITE.
   C. INK SHALL NOT BE APPLIED TO ANY SOLDERABLE SURFACE.

6. ELECTRICAL TEST:
   A. DESIGN VERIFICATION SHALL BE DONE PRIOR TO PCB FABRICATION USING SUPPLIED VALOR ODB++ DATABASE, OR GERBER DATA AND AN IPC-D-356 NETLIST.
   B. ALL PCBS SHALL BE 100% ELECTRICALLY TESTED FOR OPENS AND SHORTS USING PROVIDED DATA.
   C. APPLY TEST STAMP IN NON-LEGEND AREA ON REAR SIDE OF PCB; OK TO APPLY TO PANEL RAILS IF SPACE DOES NOT PERMIT.

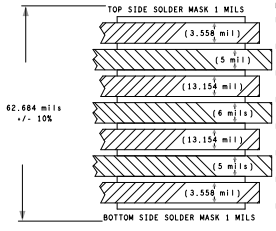
   FINAL FINISH:
   A. FINAL FINISH SHALL BE ELECTROLESS NICKEL/IMMERSION GOLD (ENIG) PER IPC-4552.

8. IMPEDANCE:
   A. IMPEDANCE TOLERANCE SHALL BE +/- 10%.
   B. SEE LAYER STACK-UP FOR IMPEDANCE REQUIREMENTS.

9. IF PANELIZATION SPECIFICATIONS ARE PROVIDED, THE PCBS SHALL BE DELIVERED IN PANEL FORM. HOWEVER, THESE SPECIFICATIONS MAY BE CHANGED AS REQUIRED BY THE CONTRACT MANUFACTURER TO SUPPORT VOLUME ASSEMBLY REQUIREMENTS.

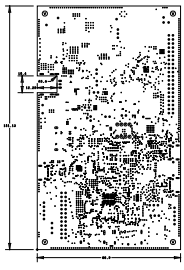
10. ENSURE ALL THE VIA HOLES ARE FILLED WITH SOLDER MASK TO ENSURE GOOD QUALITY OF SILK SCREEN PRINT.

11. ALL THE UNITS OF MEASUREMENTS[MM]



LAYER DESCRIPTION	COPPER +PLATING WT	SE IMP OHMS	SE TRACE WIDTH	DIFF IMP OHMS	DIFF TRACE WIDTH/SPACE
L01 - TOP	1.85 mils	50ohm	5.9 mils	90ohm 100ohm	4.5 mils/4.5 mils 4 mils/6 mils
L02 - GND1	1.26 mils	--	-----	---	----
L03 - SIG1	1.26 mils	50ohm	5.3 mils	---	.-----/.-----
L04 - PWR1	1.26 mils	--	-----	---	----
L05 - PWR2	1.26 mils	--	-----	---	----
L06 - SIG2	1.26 mils	50ohm	5.3 mils	---	.-----/.-----
L07 - GND2	1.26 mils	--	-----	---	----
L08 - BOTTOM	1.85 mils	50ohm	5.9 mils	90ohm 100ohm	4.5 mils/4.5 mils 4 mils/6 mils

STACK-UP





SEE BOM	SEE BOM
NEXT ASSY	USED ON
APPLICATION	

THE INFORMATION CONTAINED HEREIN IS CONFIDENTIAL AND PROPRIETARY TO CYPRESS SEMICONDUCTOR AND SHALL NOT BE REPRODUCED OR DISCLOSED IN WHOLE OR IN PART OR USED FOR ANY DESIGN, MANUFACTURE, OR OTHERWISE, EXCEPT WHEN SUCH USER POSSESSES AUTHORIZATION FROM CYPRESS

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN METRIC WITH INCHES IN BRACKETS
.XXX ±.004 [±.005] .XX ±.13 [±.01] ANGLES ±.5°

MATERIAL 2

FINISH 7

DO NOT SCALE DRAWING

APPROVALS	DATE
DRAWN: SANDHYA	05/10/23
ENGINEER: VIKU	05/10/23
APPROVED: GUBILI	05/10/23
QA: -	-
PROJ. ENG.: -	-

198 CHAMPION COURT
SAN JOSE, CA 95134
(408) 943-2600

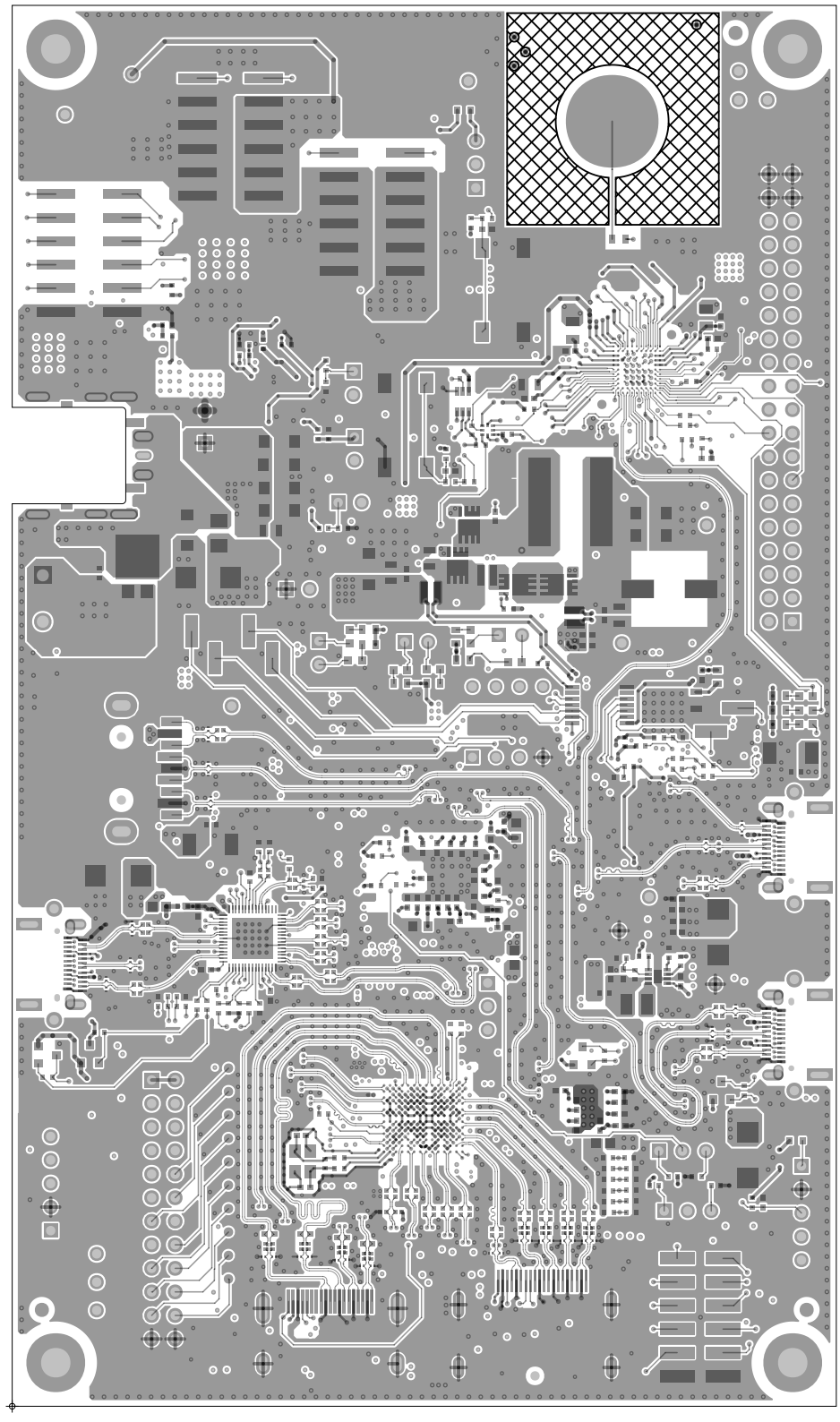
TITLE: EZ-PD PMG1-S3 USB-C Dock

SIZE	CAGE CODE	DWG NO
D	-	610-XXXXXX-01

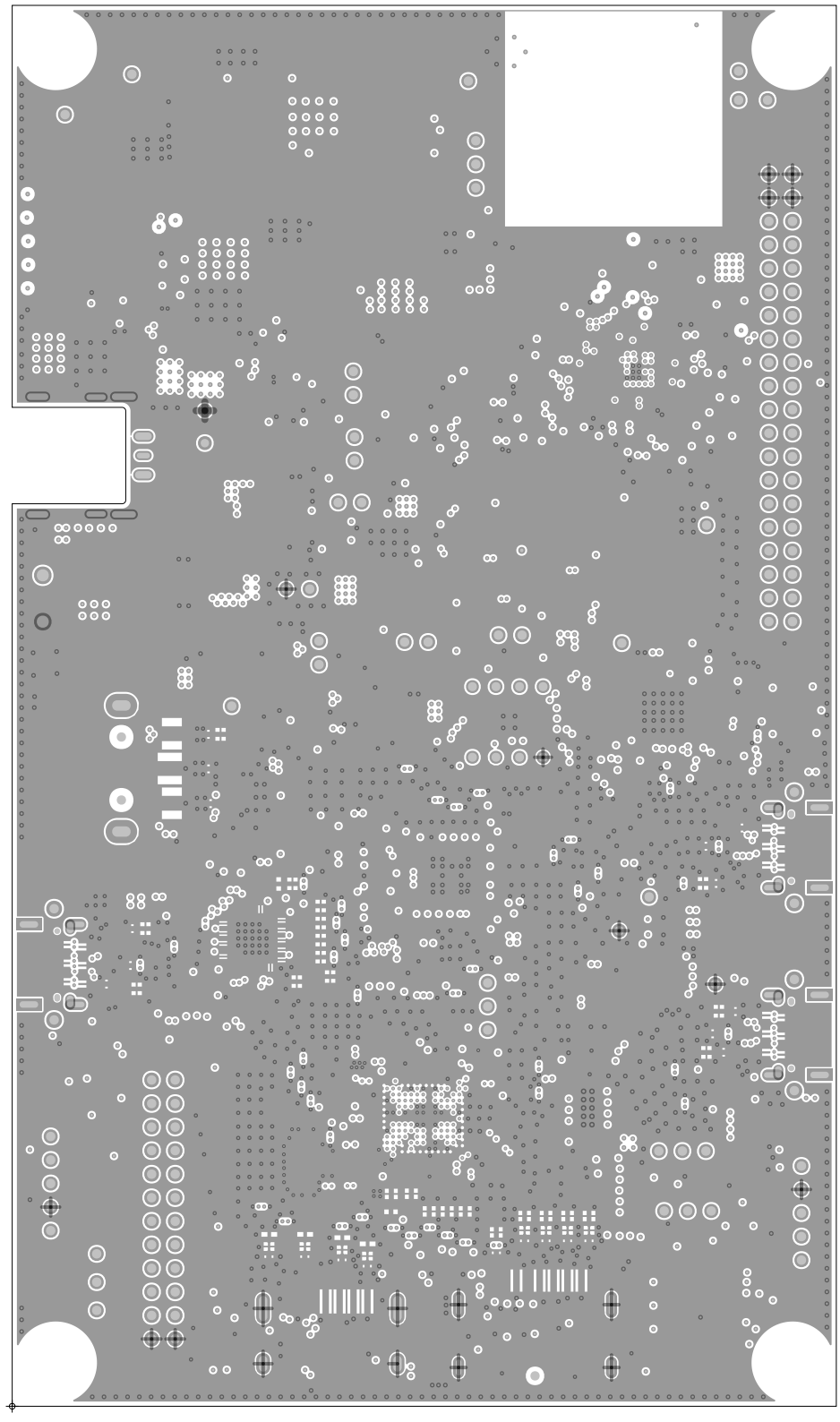
SCALE	SHEET	REV
1/1	1 OF 1	03

COMPUTER GENERATED DRAWING
DO NOT CHANGE MANUALLY

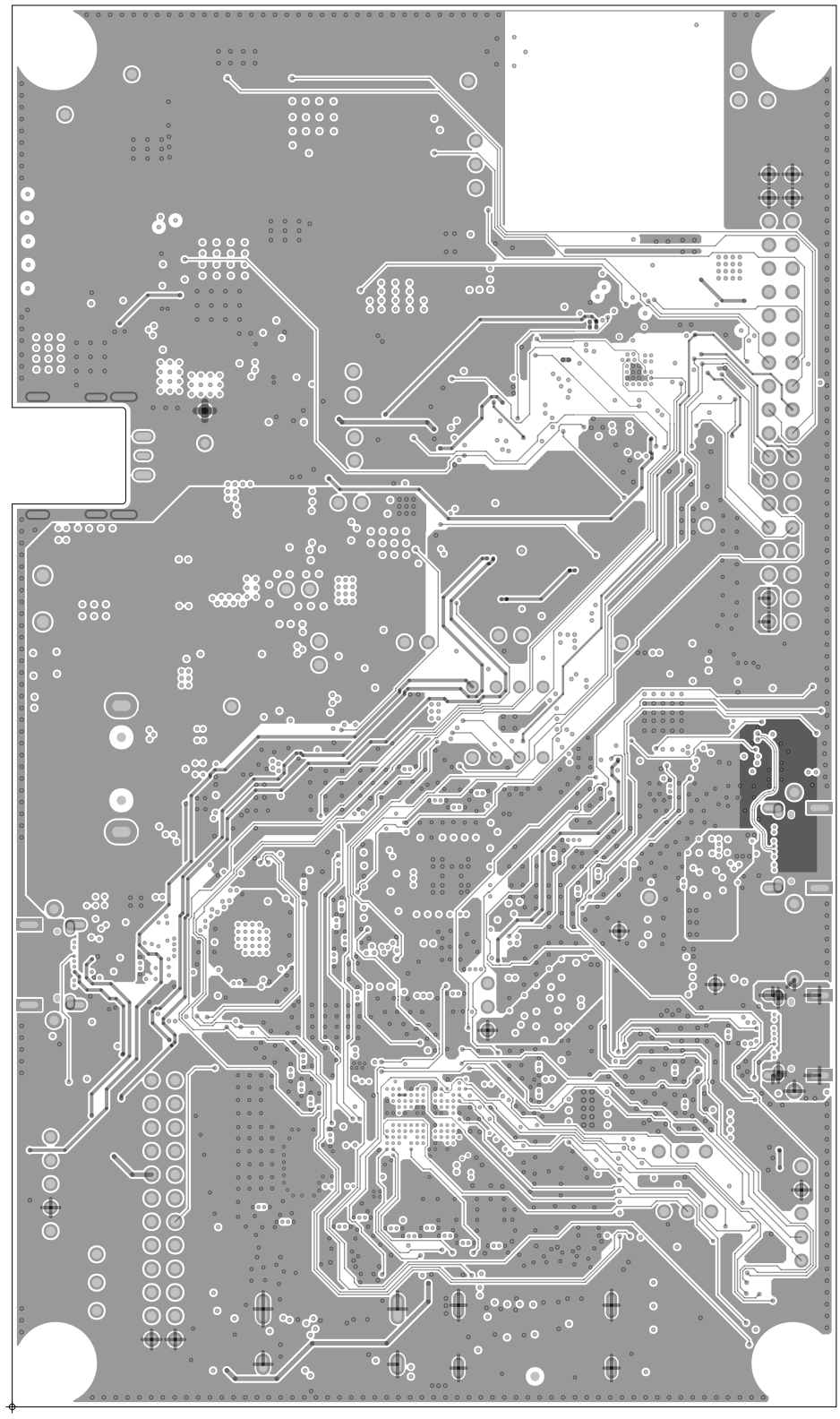
FAB NOTES REV 04/05/17



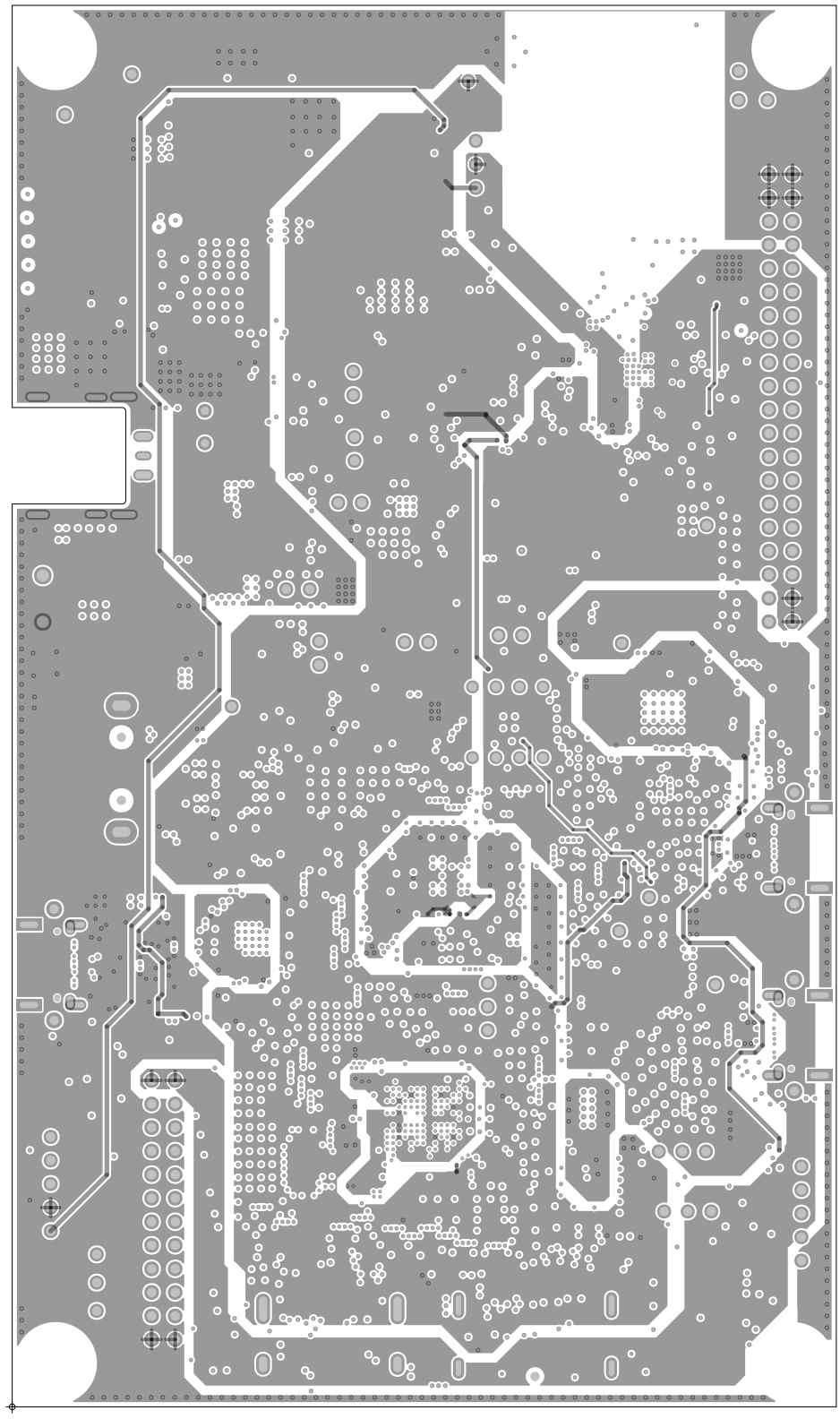
Infineon SAN JOSE, CA 95134 (408) 943-2600	PROJECT: EZ-PD PMG1-S3 USB-C Dock		
	DRAWN BY: SANDHYA		
	LAYER: 01_TOP		
	DWG NO: 610-XXXXX-01	REV: 03	DATE: 05/10/23



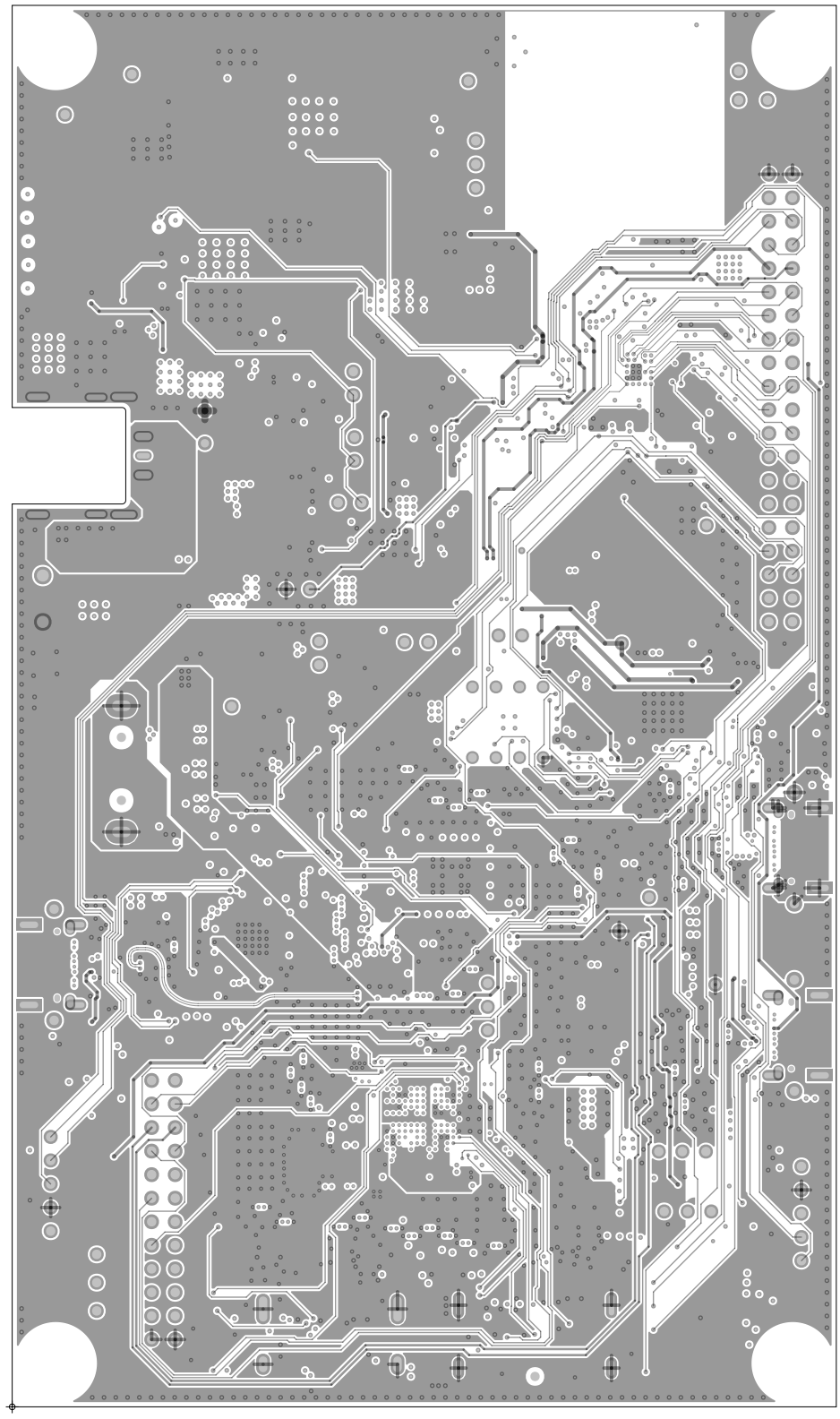
Infineon SAN JOSE, CA 95134 (408) 943-2600	PROJECT: EZ-PD PMG1-S3 USB-C Dock		
	DRAWN BY: SANDHYA		
	LAYER: 02_GND1		
	DWG NO: 610-XXXXX-01	REV: 03	DATE: 05/10/23



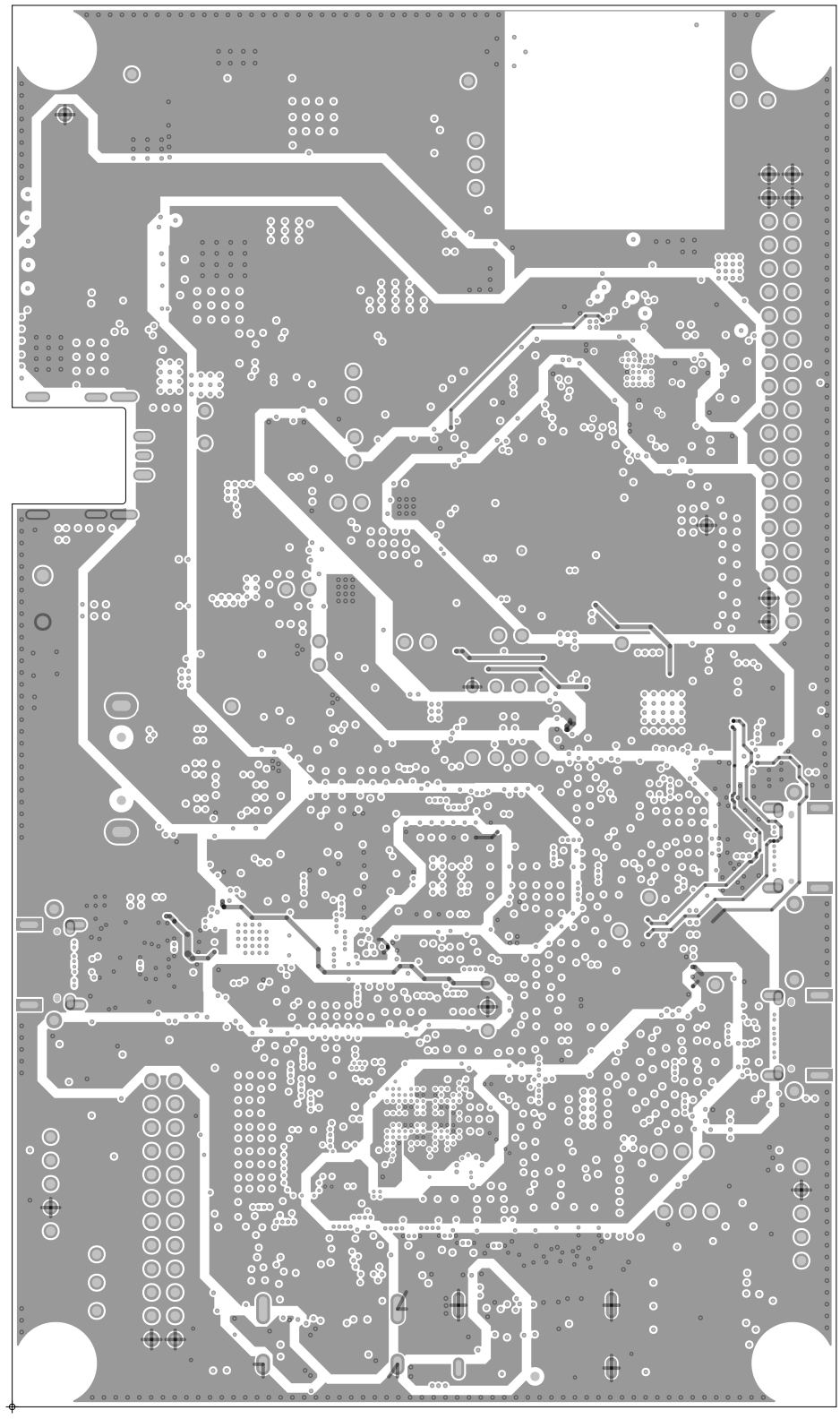
Infineon SAN JOSE, CA 95134 (408) 943-2600	PROJECT: EZ-PD PMG1-S3 USB-C Dock		
	DRAWN BY: SANDHYA		
	LAYER: 03_SIG1		
	DWG NO: 610-XXXXX-01	REV: 03	DATE: 05/10/23



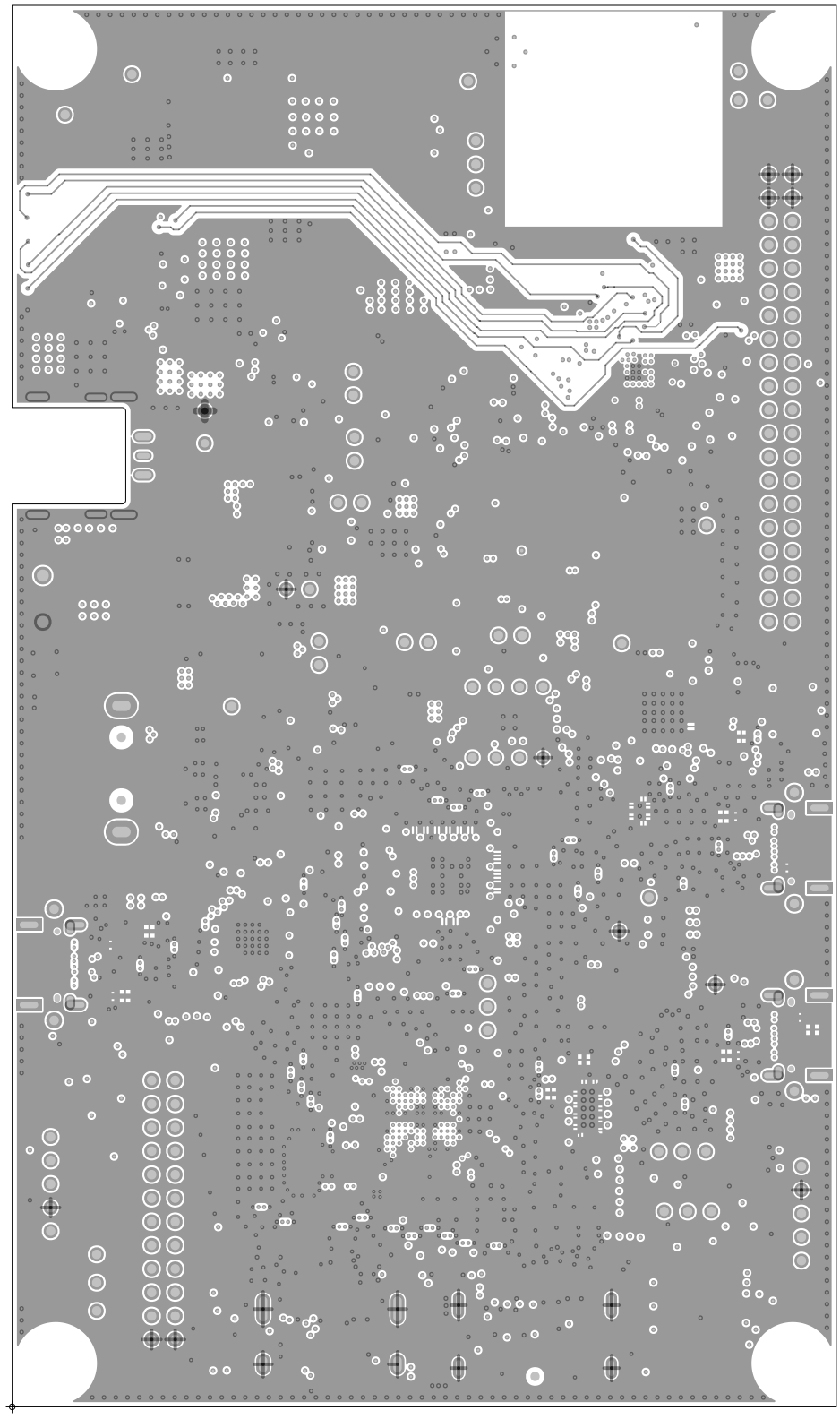
Infineon SAN JOSE, CA 95134 (408) 943-2600	PROJECT: EZ-PD PMG1-S3 USB-C Dock		
	DRAWN BY: SANDHYA		
	LAYER: 04_PWR1		
	DWG NO: 610-XXXXX-01	REV: 03	DATE: 05/10/23



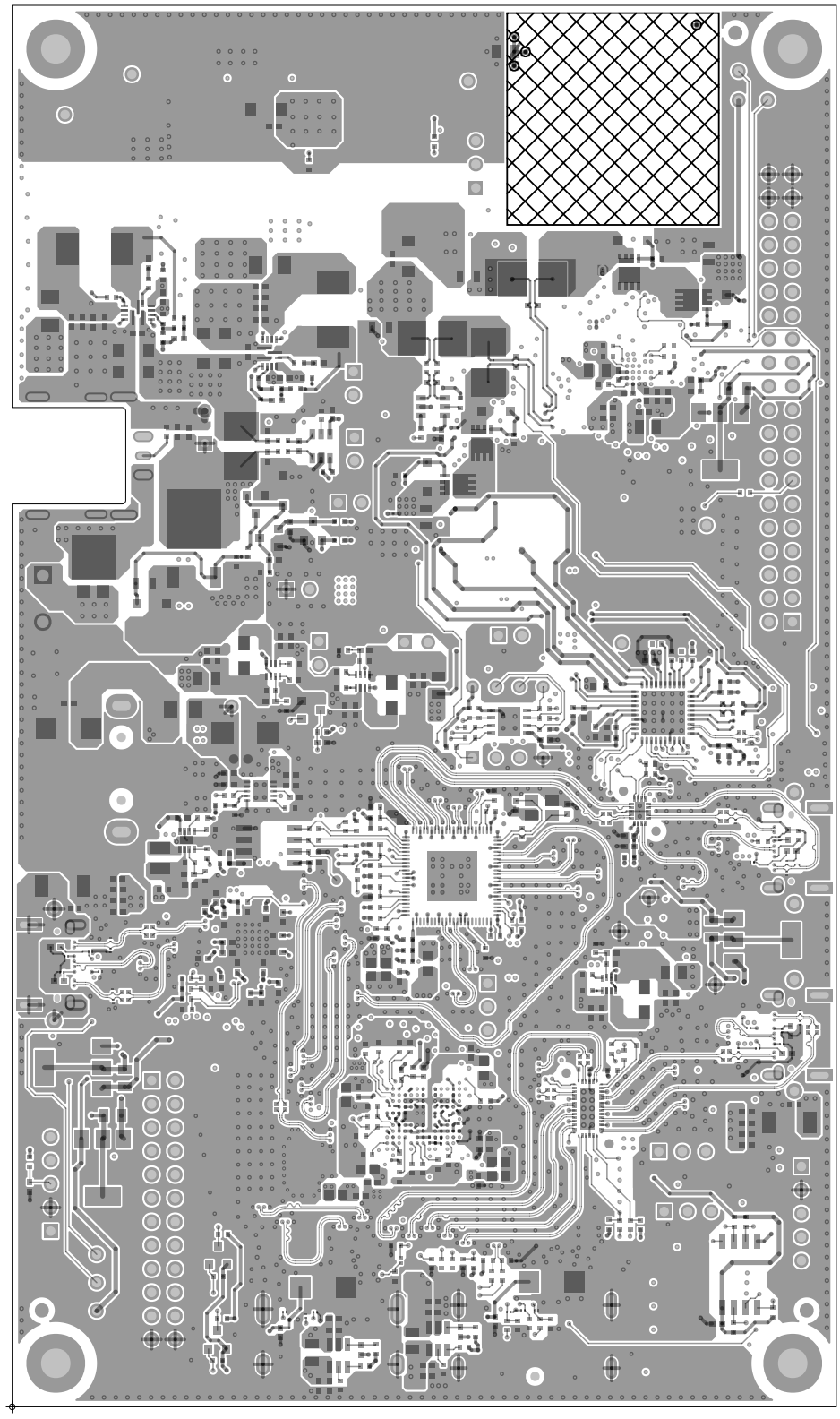
Infineon SAN JOSE, CA 95134 (408) 943-2600	PROJECT: EZ-PD PMG1-S3 USB-C Dock		
	DRAWN BY: SANDHYA		
	LAYER: 06_SIG2		
	DWG NO: 610-XXXXX-01	REV: 03	DATE: 05/10/23



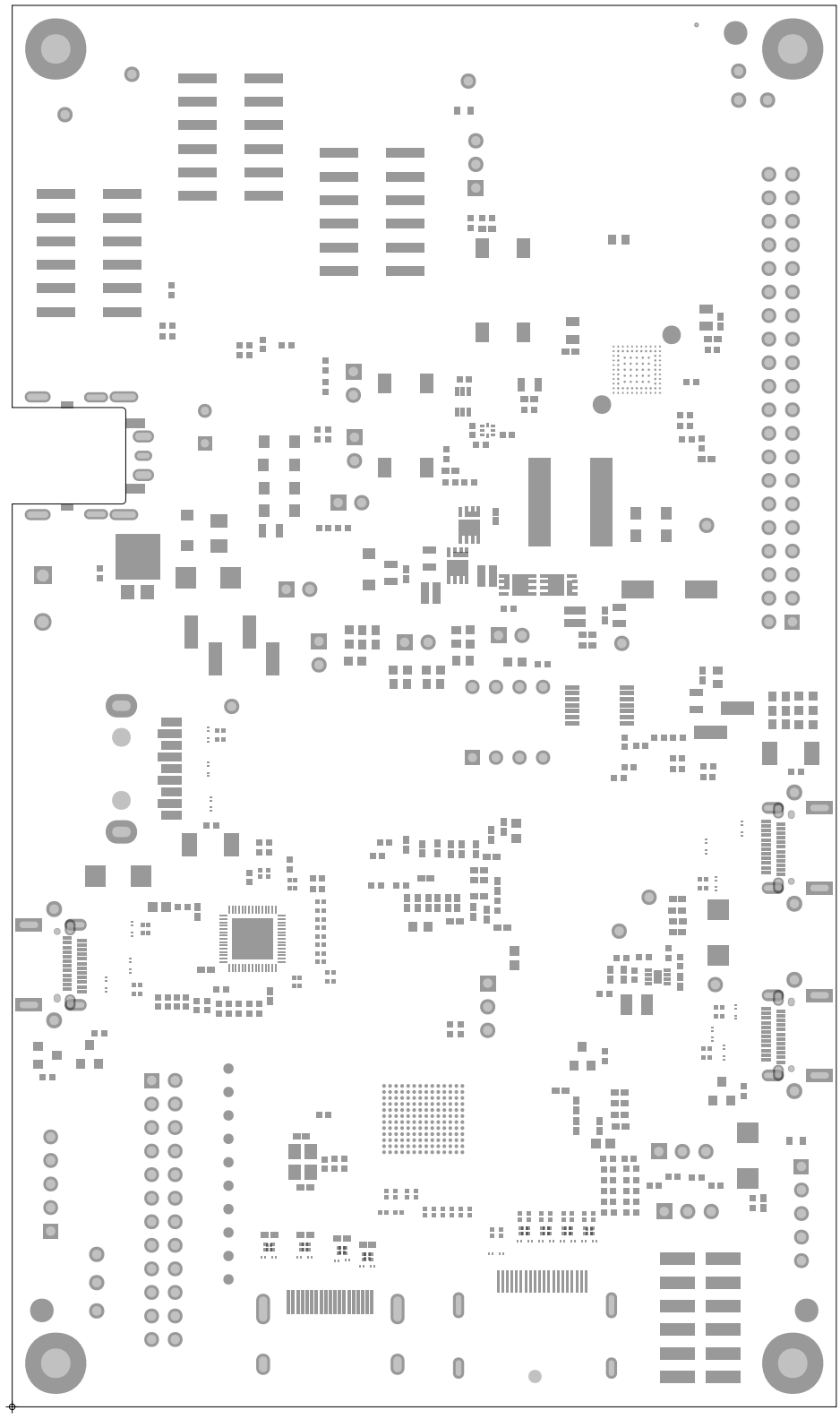
Infineon SAN JOSE, CA 95134 (408) 943-2600	PROJECT: EZ-PD PMG1-S3 USB-C Dock		
	DRAWN BY: SANDHYA		
	LAYER: 05_PWR2		
	DWG NO: 610-XXXXX-01	REV: 03	DATE: 05/10/23



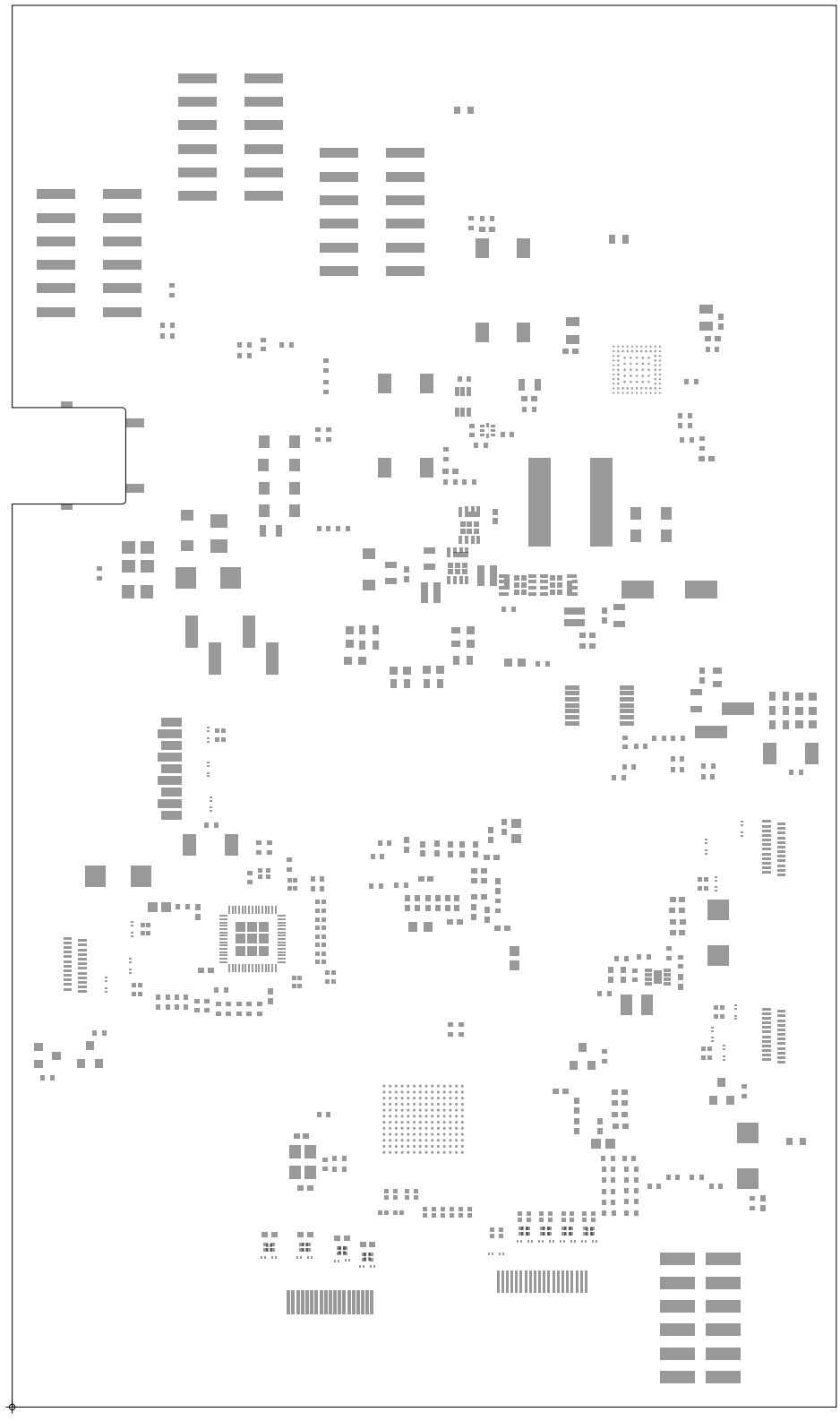
Infineon SAN JOSE, CA 95134 (408) 943-2600	PROJECT: EZ-PD PMG1-S3 USB-C Dock		
	DRAWN BY: SANDHYA		
	LAYER: 07_GND2		
	DWG NO: 610-XXXXX-01	REV: 03	DATE: 05/10/23



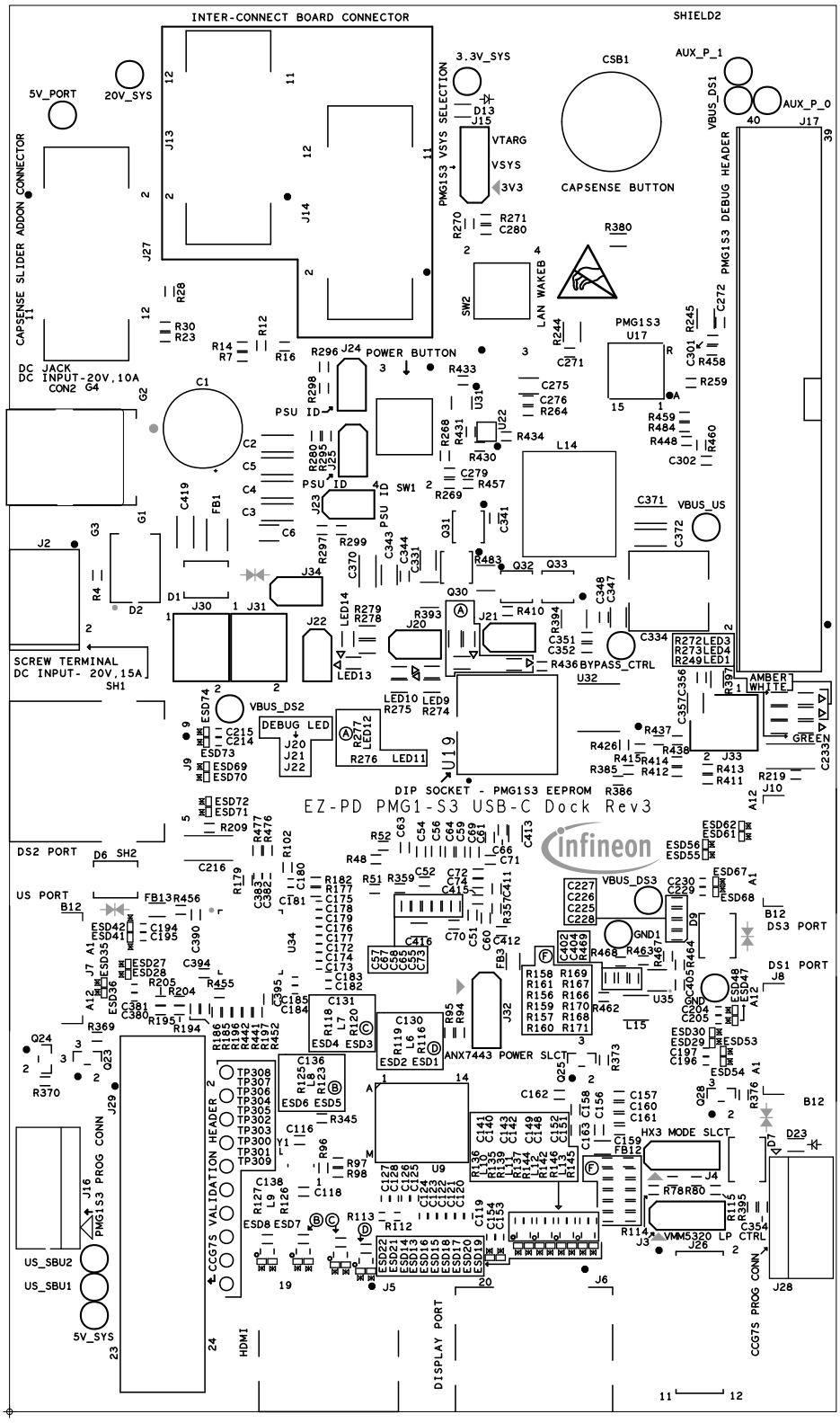
Infineon SAN JOSE, CA 95134 (408) 943-2600	PROJECT: EZ-PD PMG1-S3 USB-C Dock		
	DRAWN BY: SANDHYA		
	LAYER: BOTTOM		
	DWG NO: 610-XXXXX-01	REV: 03	DATE: 05/10/23



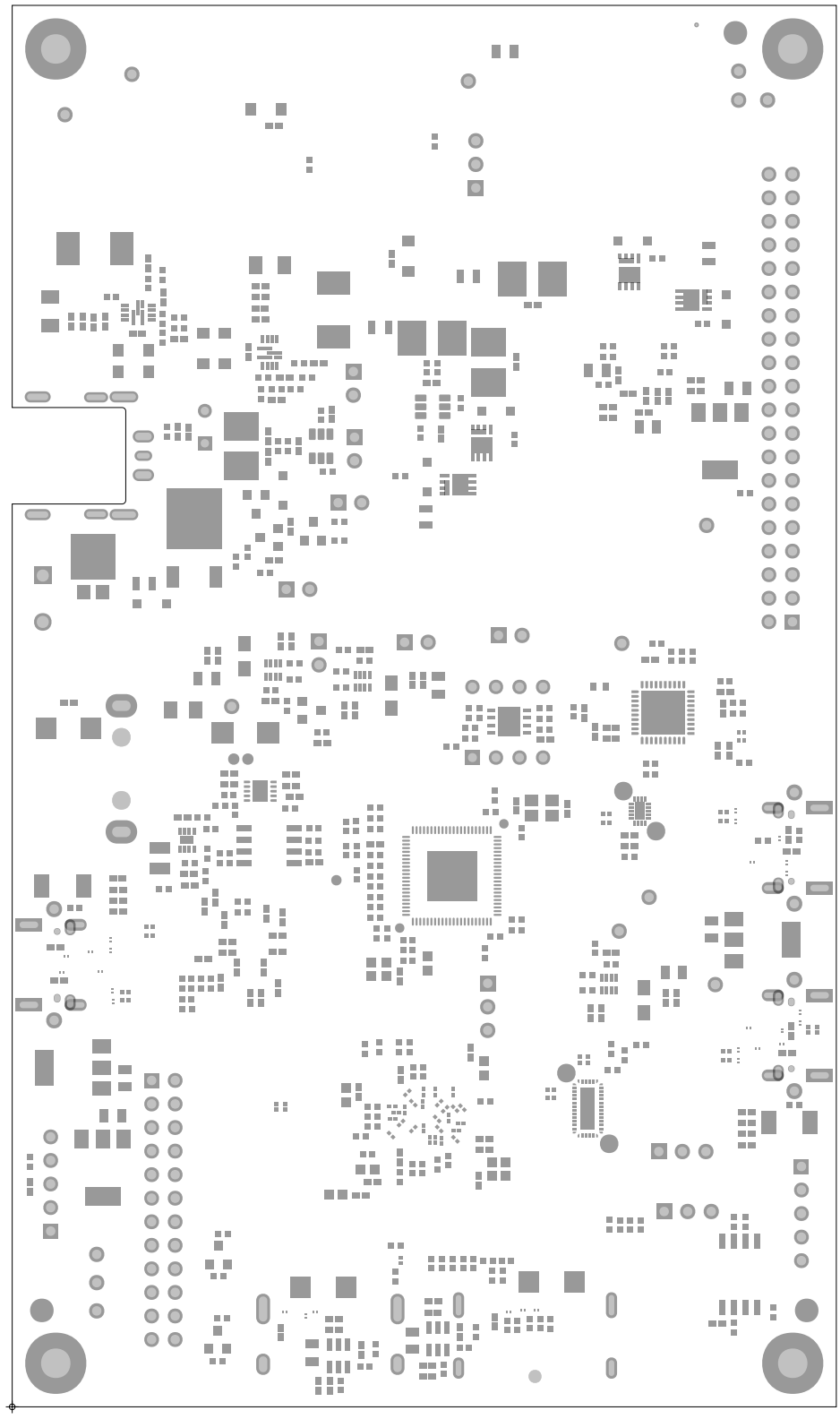
Infineon SAN JOSE, CA 95134 (408) 943-2600	PROJECT: EZ-PD PMG1-S3 USB-C Dock		
	DRAWN BY: SANDHYA		
	LAYER: SOLDERMASK TOP		
	DWG NO: 610-XXXXX-01	REV: 03	DATE: 05/10/23



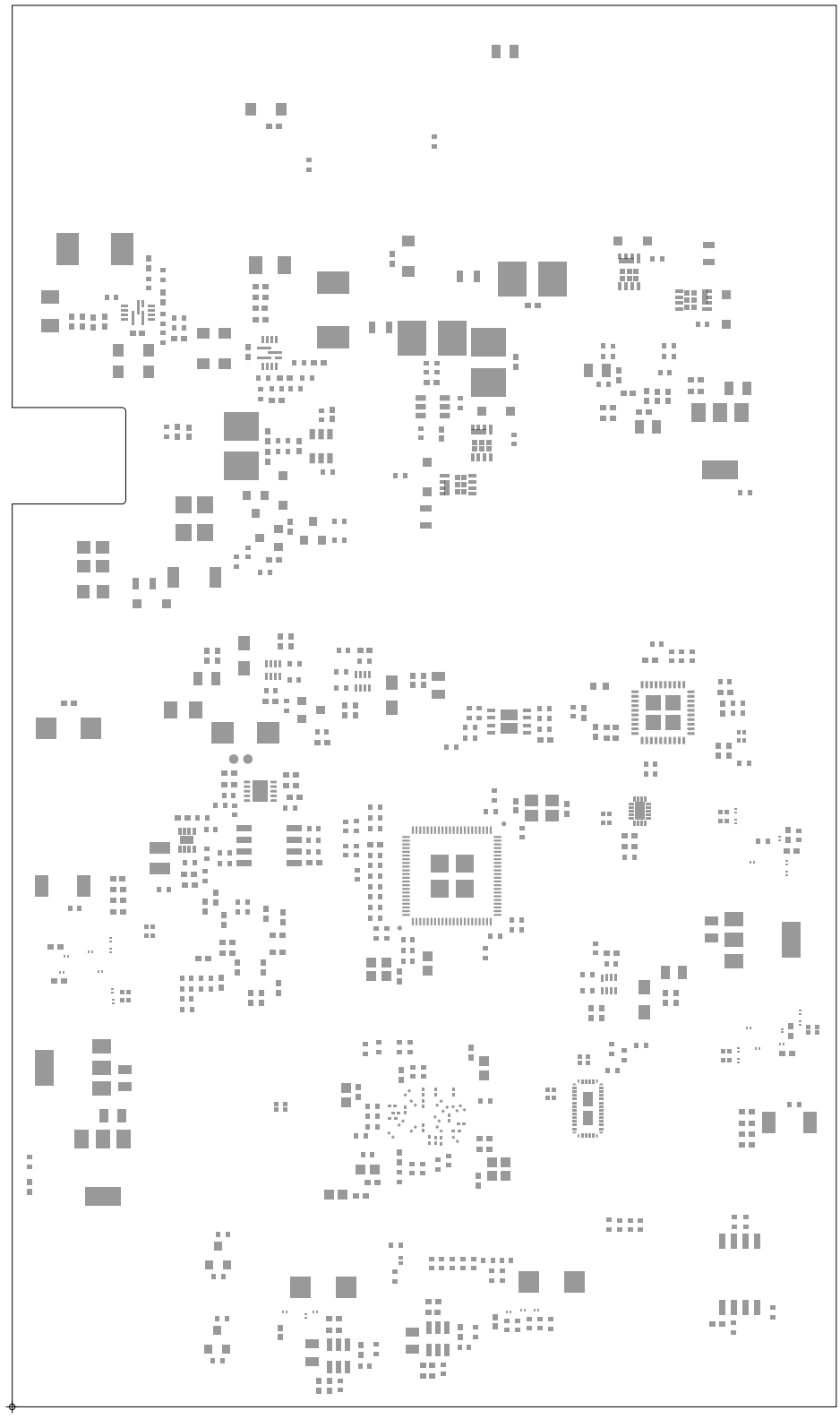
Infineon SAN JOSE, CA 95134 (408) 943-2600	PROJECT: EZ-PD PMG1-S3 USB-C Dock		
	DRAWN BY: SANDHYA		
	LAYER: PASTEMASK TOP		
	DWG NO: 610-XXXXX-01	REV: 03	DATE: 05/10/23



SAN JOSE, CA 95134 (408) 943-2600	PROJECT: EZ-PD PMG1-S3 USB-C Dock		
	DRAWN BY: SANDHYA		
	LAYER: SILKSCREEN TOP		
	DWG NO: 610-XXXXX-01	REV: 03	DATE: 05/10/23



Infineon SAN JOSE, CA 95134 (408) 943-2600	PROJECT: EZ-PD PMG1-S3 USB-C Dock			
	DRAWN BY: SANDHYA			
	LAYER: SOLDERMASK BOTTOM			
	DWG NO: 610-XXXXX-01	REV: 03	DATE: 05/10/23	



Infineon SAN JOSE, CA 95134 (408) 943-2600	PROJECT: EZ-PD PMG1-S3 USB-C Dock		
	DRAWN BY: SANDHYA		
	LAYER: PASTEMASK BOTTOM		
	DWG NO: 610-XXXXX-01	REV: 03	DATE: 05/10/23

PROJECT: EZ-PD PMG1-S3 USB-C Dock		
DRAWN BY: SANDHYA		
LAYER: SILKSCREEN BOTTOM		
DWG NO: 610-XXXXX-01	REV: 03	DATE: 05\10\23

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